Configuring the VideoIR M series Cameras for Pixel Sync Gen Lock

- Use the BFCOM.exe to configure the **VideoIR M series** cameras.
- The **VideoIR M series** Cameras can output **Pixel Sync Gen Lock** signals (master mode) or receive **Pixel Sync Gen Lock** signals (slave mode).
- The configuration of master vs slave is through the serial port.

The serial protocol uses:-

- one start bit,
- one stop bit,
- no parity,
- no handshaking
- a baud rate of 115 Kbps or 38.4 Kbps. (The baud rate setting is set at the factory – 115Kbps).

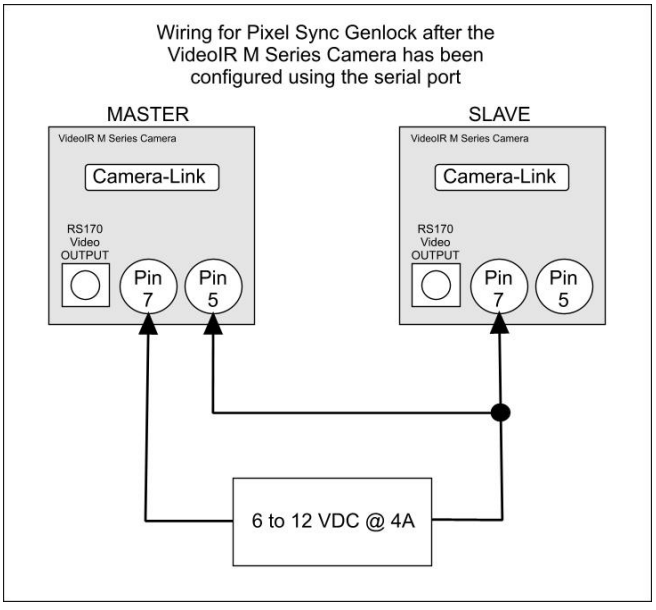
- By default, the **VideoIR M series** sensor runs in master mode and does not have the **Pixel Sync Gen Lock** circuitry enabled. In order to configure a **VideoIR M series** sensor to be a **Pixel Sync Gen Lock** master (and to output the **Pixel Sync Gen Lock** signals), the following commands should be sent via the serial port.

conf GenlockSignalSource 1 STORE
CONF:GLOC:WAIT OFF

- In order to configure a **VideoIR M series** sensor to be a gen -lock slave, the following commands should be sent via the serial port.

conf GenlockSignalSource 1 STORE
CONF:GLOC:WAIT 10

- After **Pixel Sync Gen Lock** commands are sent, the **VideoIR M series** camera must be power-cycled for the commands to take effect.
- Each Camera will need a separate dedicated Frame Grabber installed in the system.
- Each camera needs to be configured separately.



Wiring for use with Pixel Sync Gen Lock